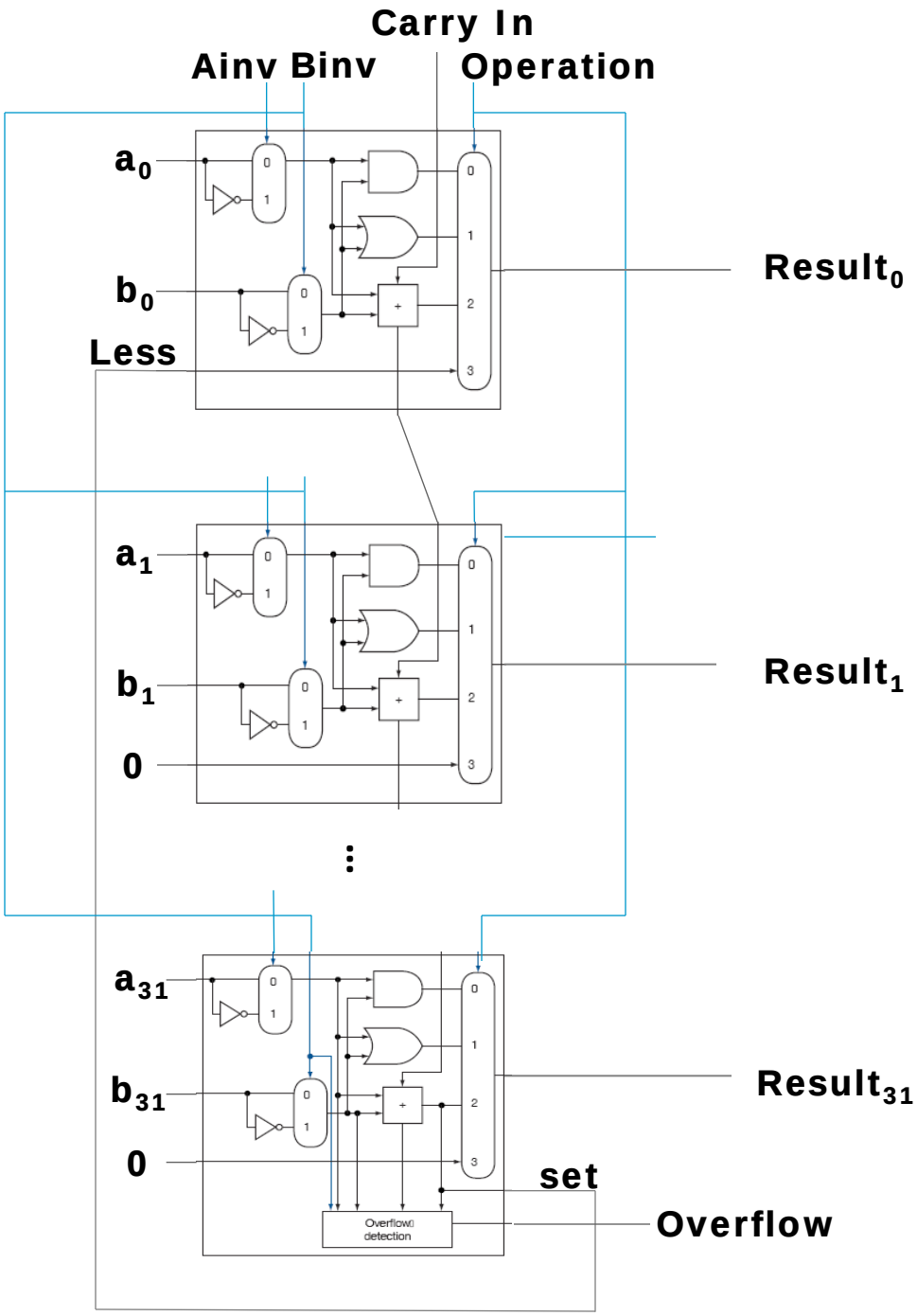
For two 32-bit quantities **a** and **b**.

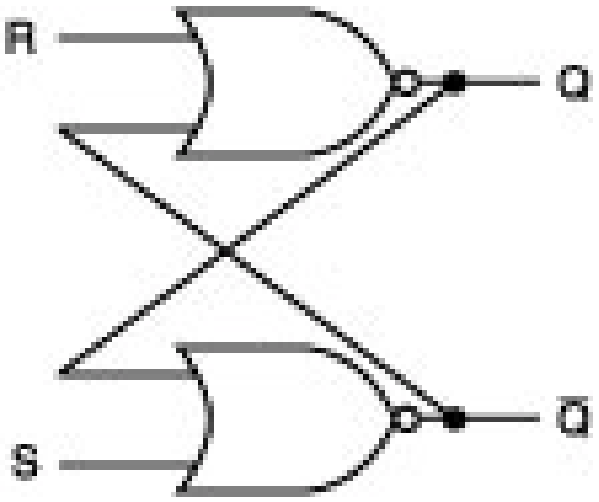
What are the settings to compute:

- **a** + **b**
- **a** – **b**
- **a** < **b**
- **a** or **b**
- **a** and **b**
- **a** nor **b**
- **a** nand **b**

Trace on 2 < 3

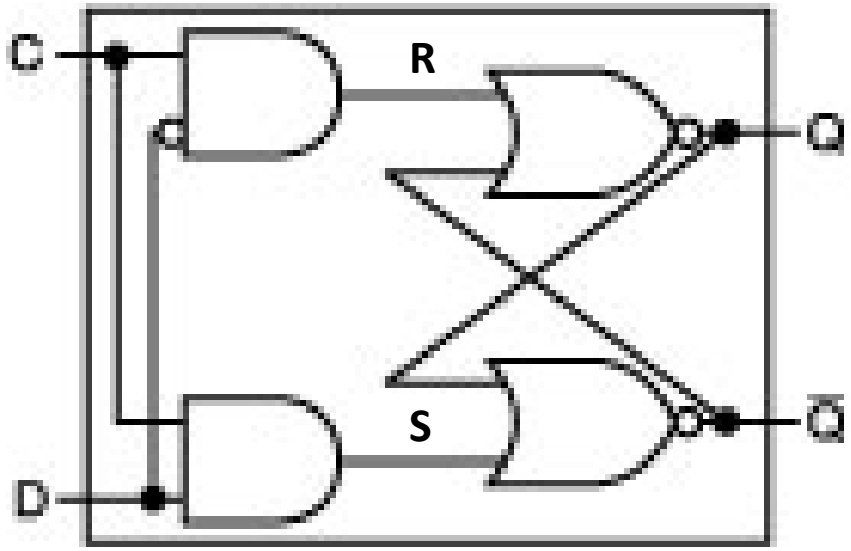
```
li $t0, 2
slti $t1, $t0, 3
```



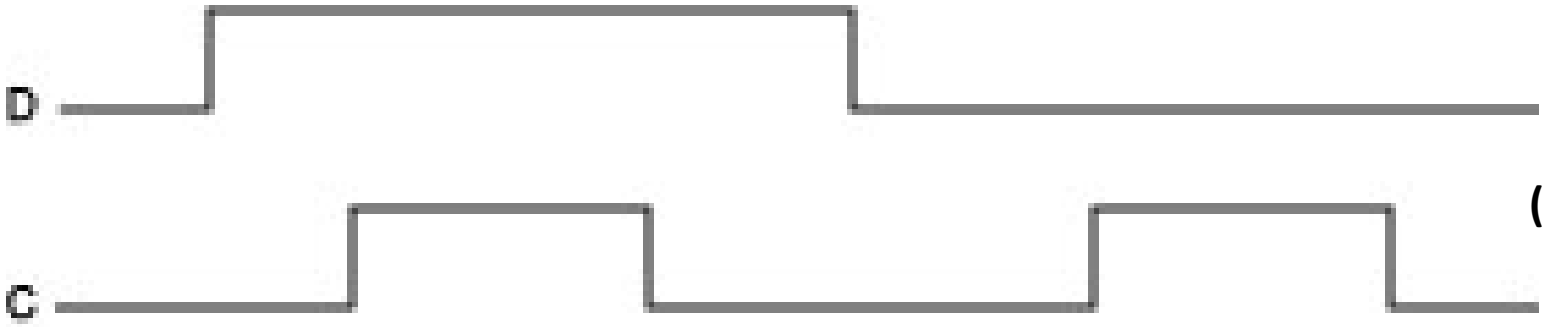


S-R Latch

Write Enable



D Latch



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