For two 32-bit quantities $a$ and $b$.

What are the settings to compute:

- $a + b$
- $a - b$
- $a < b$
- $a$ or $b$
- $a$ and $b$
- $a$ nor $b$
- $a$ nand $b$

Trace on $2 < 3$

```assembly
li $t0, 2
slti $t1, $t0, 3
```
S-R Latch